

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)	Docket Number (Optional) YOR9200010217US2	Application Number 15692065 Not yet assigned
	Applicant Magerlein et al.	
	Filing Date Concurrently herewith	Group Art Unit Not yet assigned 2818

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DN	6,339,024	1/15/02	Petrarca et al.	438	686	—
	6,344,125	2/5/02	Locke et al.	205	118	—
	6,368,484	4/9/02	Volant et al.	205	220	—
	6,137,185	10-2002	Ishino et al.	257	786	—
	6,373,273	04-2002	Akram et al.	324	765	—
DN	5,808,360	09-1998	Akram et al.	257	738	—

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

DN	Richard Volant, Kevin Petrarca, Peter Locke, James A. Tornello and Donald F. Canaperi, Title: "Replated Metal Structures for Semiconductor Devices", U.S. Patent application Serial No. 09/657,469, filed on May 9, 2000.
EXAMINER	DATE CONSIDERED 7/28/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.